**March 29th Senior Project Meeting**

**Armstrong Hall 148: 2:00PM –**

**Members in Attendance:** Zach, Dhruvit, Kevin, Whitley, and Dr. Pearlstein

* Zach
  + Last Week
    - Got all correct port mappings
    - Fixed all but one FPGA warnings
    - Ian finished PSoC program and show me how to use it
      * I tested it and it seems to work
    - Wrote FPGA guide
    - Created a realistic test environment with I2S input stream
      * Nothing on I2S output (might be a reset issue)
    - Updated website
  + This Week
    - Further testing and debugging of FPGA
    - More code documentation
* Dhruvit
  + Last Week
    - Drew a transistor in EDA tools
  + This Week
    - Design rule check
    - Research on how to make inductor
    - Power and ground routing
    - MOSIS GSDII file requirements
    - Implementation warning
* Kevin
  + Last Week
    - Fixed warnings
  + This Week
    - Help Dhruvit
* Whitley
  + Last Week
    - Fixed I2C warnings
    - PSoC I2C program
  + This Week
    - I2C program
    - Default register state
* Julie
  + Last Week
    - Playing around and learning the EDA tool
    - Fixed overrun/underrun setting that Kevin told me to correct
    - Added comments to code
  + This Week
    - Help Dhruvit